Desired Characteristics:

- Accuracy
- Linearity
- Low Offset Error
- Monotonicity
- Speed -> Low Settling Time
- Resolution -> # of Bits
- Low Temperature Coefficients
- Power Supply Rejection
R-2R approaches
Analog Signal (Current or Voltage) Input from 'Event' 

A/D Converter

Enable Convert
MSB
Logic Levels out to μP Bus
LSB
Power

Desired Characteristics:

- Accuracy
- Linearity
- Low Offset Error
- Monotonicity
- Speed -> Low Settling Time
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- Low Temperature Coefficients
- Power Supply Rejection

Vref

R

Vin

R

Vout
8-Bit Half-Flash A/D

8-Bit Successive Approximation A/D
V/F converter

Single Slope A/D